

# **ABSTRACT OF THE DISCLOSURE**

This invention relates to a CCD pulse generator capable of generating a CCD driving signal and CCD output processing signal at accurate timings. The pulse generator  
5 includes a digital delay type pulse control section which finely delays a transfer signal for driving a CCD, thereby generating a plurality of delay signals, and changes selection of these delay signals, thereby generating a CCD driving signal and/or CCD output processing signal  
10 corresponding to set rise and fall timings, a turned-over/untuned-over signal generating section which generates the turned-over signal and untuned-over signal of the generated signal, a selection section which selects the turned-over signal and untuned-over signal of the  
15 generated signal, a blanking section which temporarily disables the generated signal, an output section which has an output enable function and outputs the signal selected by the selection section, and an output signal condition setting section which sets pieces of condition setting  
20 information that determine the operations of the respective sections.